

**SYSTEM AND METHOD FOR THE HANDLING OF SYSTEM MANAGEMENT
INTERRUPTS IN A MULTIPROCESSOR COMPUTER SYSTEM**

ABSTRACT OF THE DISCLOSURE

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A method for handling system management interrupts in multiprocessors systems is provided. After the processors of the system enter system management mode, one of the processors of the system is designated to handle the system management interrupt. The processor designated to handle the system management interrupt scans a memory location that includes a memory space associated with the saved contents of the processor registers of each processor. After locating a SMI signature in one of the memory spaces associated with the respective processors of the system, the SMI handler of the processor designated to handle the system management interrupts, retrieves any necessary parameters for the system management interrupt from the memory space associated with the SMI signature, thereby allowing a processor to cause the issuance a SMI, to pass a set of parameters for the software SMI, and to permit a second processor to receive the parameters and handle the software SMI.

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